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8/16/05 SM

### IN THE CLAIMS

Please enter the following amendments.

1. (Currently Amended) A method for providing a response to a cache access request, the method comprising:

receiving a cache access request associated with a memory line at a cache coherence controller from a processor in a cluster of processors, the cluster of processors interconnected in a point-to-point architecture;

obtaining response information for the cache access request from a remote data cache associated with the cache coherence controller;

determining that the cache access request can be handled locally by using the remote data cache without having to probe remote nodes; and

providing response information with a completion indicator to the processor when it is determined that the cache access request can be handled locally.

2. (Currently Amended) The method of claim 1, wherein the cache access request can be handled locally if a valid copy of the memory line is in the remote data cache, wherein response information is provided in a response packet.

3. (Original) The method of claim 1, wherein response information includes state information.

4. (Currently Amended) The method of claim 2, wherein the completion indicator is a completion bit ~~response information includes data.~~

5. (Original) The method of claim 1, wherein the completion indicator notifies the processor that the response from the cache coherence controller will be the only response.

6. (Original) The method of claim 1, wherein the processor is a request processor in a request cluster.

7. (Original) The method of claim 1, wherein the completion indicator allows the cache coherence controller to avoid probing local or remote nodes.

8. (Original) The method of claim 1, wherein the processor sends a source done upon identifying the completion indicator in the response.

9. (Original) The method of claim 8, wherein the processor sends the source done to the cache coherence controller.